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**IN THE CLAIMS:**

Please amend the claims as follows. The following claims are a complete set of the claims as amended in the preferred format of numbering each line of every claim, with each claim beginning with line one.

The following claims are presented in re-written "clean format":

*Sale C1*  
A1 1. ('Clean' version) A method of establishing thread priority in a single processor  
2 comprising:  
3 assigning a value in memory to indicate which of a plurality of threads executed by said  
4 single processor has a higher priority.

*Sale C1*  
A2 1 10. ('Clean' version) A method of establishing thread priority in a single processor  
2 comprising:  
3 assigning a value in an APIC TPR register for a thread via execution of operating system  
4 code to indicate which of a plurality of threads executed by said single processor has a higher  
5 priority.

1 11. ('Clean' version) An apparatus for establishing thread priority in a single processor  
2 comprising:

*A2*  
3        a memory to store a value to indicate which of a plurality of threads to be executed by  
4        said single processor has a higher priority.

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*A3*  
*Sale*  
*C7*  
1        20. ('Clean' version) An apparatus for establishing thread priority in a single processor  
2        comprising:  
3              an APIC TPR register for a thread wherein execution of operating system code causes a  
4              value to be stored in said register to indicate which of a plurality of threads to be executed by  
5              said single processor has a higher priority.

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